Syfer Technology has developed a range of surface mount multilayer ceramic capacitors aimed specifically at the LCD inverter market. The advantage gained over standard product is a reduced susceptibility to surface arcing which allows for the replacement of leaded components. The improved properties are achieved by the utililisation of a unique COG/NPO dielectric material.

Parts for these applications are identified with the suffix code FB9.

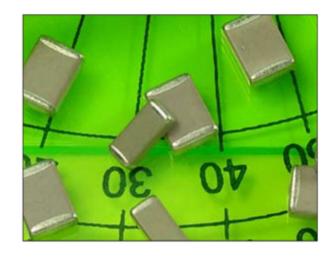
Users should carefully consider solder pad design as this can influence arcing voltage.

## Capacitance range

 1808
 1812

 5kV
 1.5pF - 22pF
 3.9pF - 68pF

 6kV
 1.5pF - 12pF
 3.9pF - 33pF



## Ordering information - LCD Inverter range

1808	Υ	5K0	0680	D	С	T	FB9
Chip size	Termination	Voltage d.c.	Capacitance in picofarads (pF)	Capacitance tolerance	Dielectric codes	Packaging	Suffix
1808 1812	Y = Nickel barrier with polymeric silver termination.	5K0 = 5kV 6K0 = 6kV	First digit is 0. Second and third digits are significant figures of capacitance code. The fourth digit is number of zeros following.  Example:  0680 = 68pF	<10pF $B = \pm 0.1pF$ $C = \pm 0.25pF$ $D = \pm 0.5pF$ $\geq 10pF$ $F = \pm 1\%$ $G = \pm 2\%$ $J = \pm 5\%$ $K = \pm 10\%$	C = COG/NPO	T = 178mm (7") reel R = 330mm (13") reel B = Bulk pack - tubs	LCD Inverter range

